



Docket No.: Z&PINFP10802

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By: 

Date: December 1, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/615,567 Confirmation No: 9016  
Applicant : Dietrich Bonart et al.  
Filed : July 8, 2003  
Title : Vertical Transistor, and Method for Producing a Vertical Transistor  
Art Unit : 2822  
Examiner : Mary A. Wilczewski

Docket No. : Z&PINFP10802  
Customer No. : 24131

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R E S P O N S E

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated November 17, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 16-21, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 16-21 is solicited.

Respectfully submitted,



For Applicants

WERNER H. STEMER  
REG. NO. 34,956

Date: December 1, 2004

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